

FORM PTO-1449 US DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		AUG 14 2000 PATENT & TRADEMARK OFFICE		Atty. Docket No. <b>81020F-P</b> Customer No. 01333		Serial No. <b>US 09/559,478</b>	
If AFTER the later date of the first Office action or 3 months from filing, use only with Rule 97(E) Certificate or Fee  <b>LIST OF ART CITED BY APPLICANT</b> (Use several sheets if necessary)				Applicant: <b>Richard A. Simon</b>			
				Filing Date <b>27 April 2000</b>		Group <b>2775</b>	
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initial*	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
<b>FOREIGN PATENT DOCUMENTS</b>							
Examiner Initial*	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES   NO	
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
<i>J.S.</i>  <i>J.S.</i>  <i>J.S.</i>	Interactive Computer Algorithms With Applications in Engineering: Solving combinatorial optimization Problems by Sait, S. M. and Youssef, H., IEEE Computer Society, Los Alamitos, Calif., 1999;						
	VLSI Module Placement Based on Rectangle-Packing by the Sequence-Pair, IEEE Trans. Computer-Aided Design, vol. 15, pp. 1518-1524, Dec. 1996						
	BSG structure described in S. Nakatake, K. Fujiyoshi, H. Murata, and Y. Kajitani, "Module Placement on BSG-Structure and IC Layout Applications," Proc. IEEE Int. Conf. Computer-Aided Design, pp. 484-191, 1996.						
EXAMINER <i>Jonathan Schlegel</i>				DATE CONSIDERED <b>9/3/2003</b>			
<small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							

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